

FIG. 1(c)

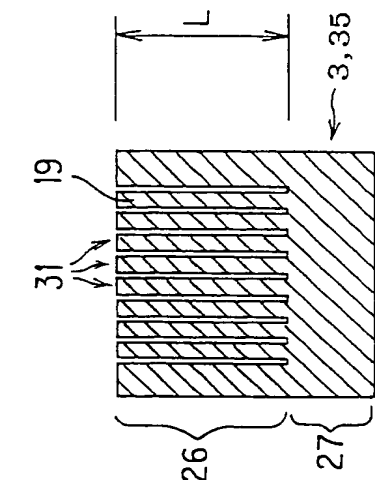


FIG. 1(a)

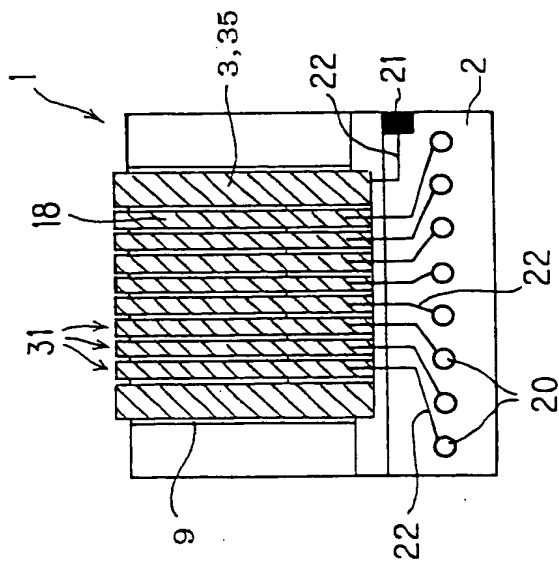


FIG. 1(b)

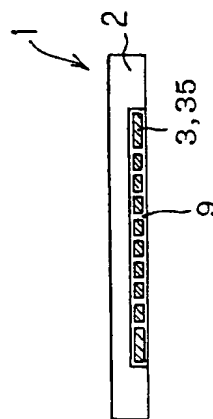
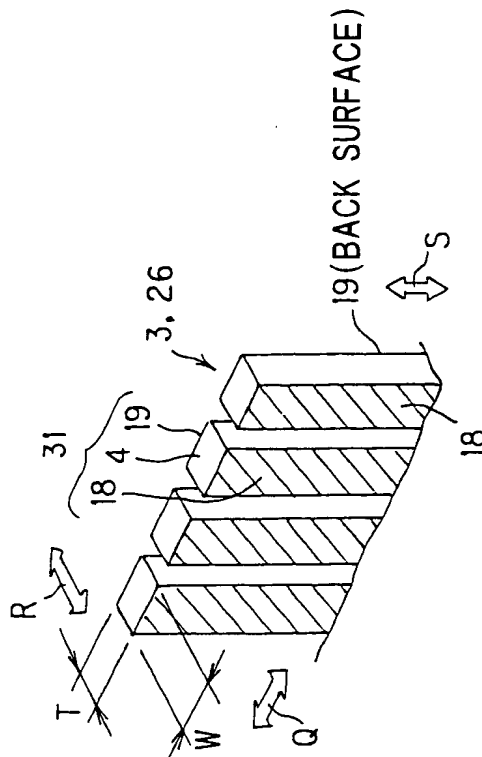


FIG. 1(d)



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FIG. 2(a)

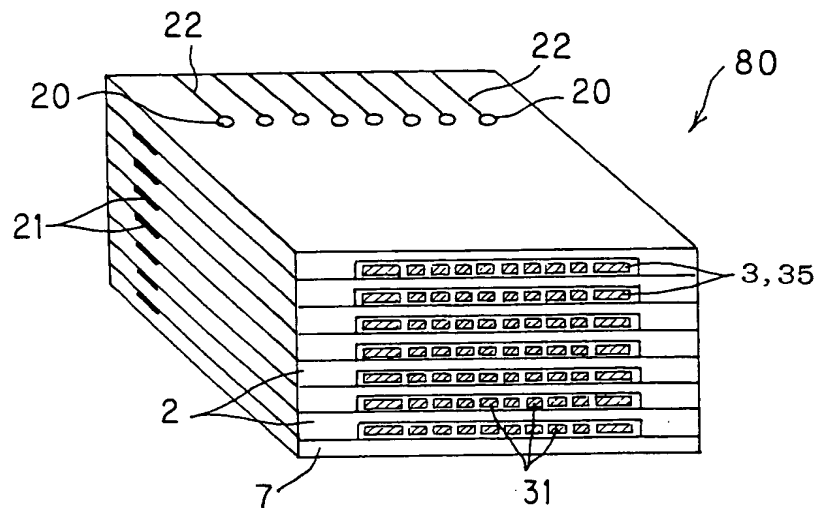


FIG. 2(b)

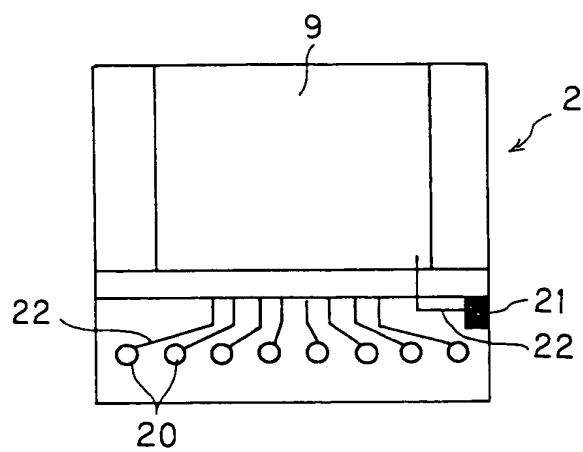


FIG. 2(c)

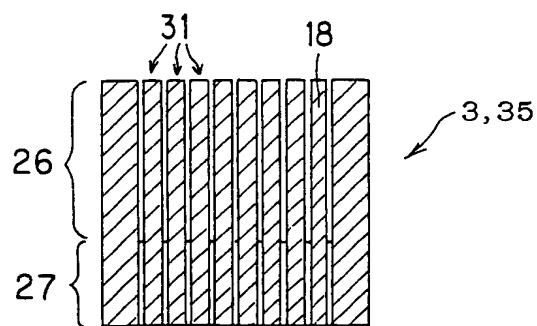


FIG. 3(a)

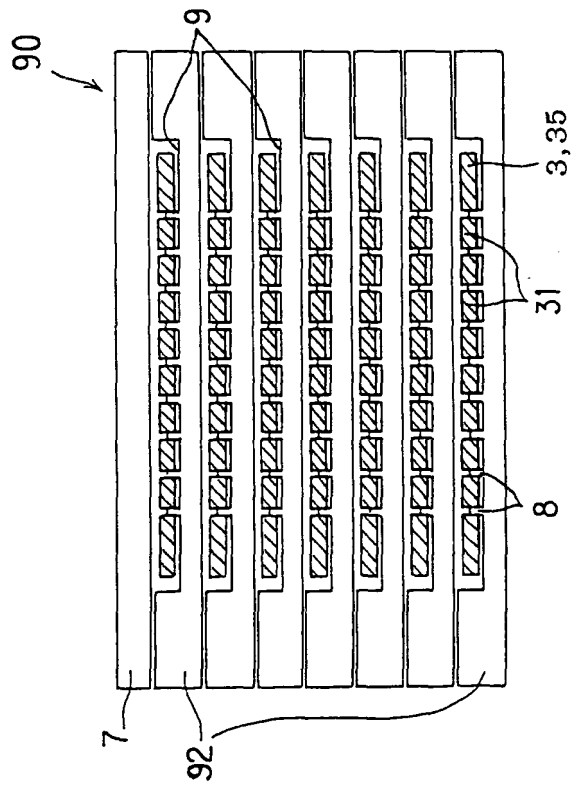


FIG. 3(b)

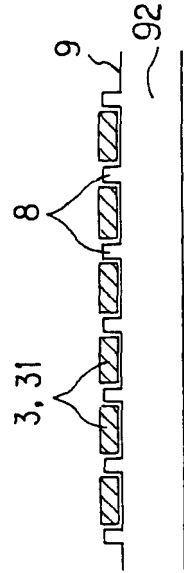
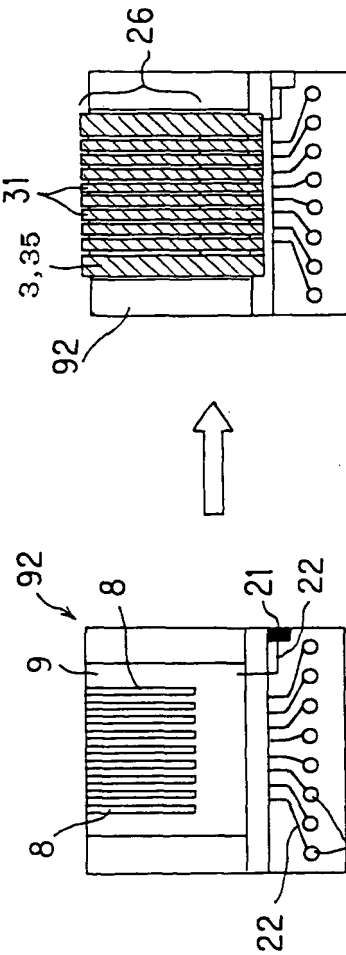


FIG. 3(c)



GUIDE SUBSTRATE ALONE

STATE IN WHICH PIEZOELECTRIC DEVICE

SUBSTRATE IS INSERTED IN GUIDE SUBSTRATE

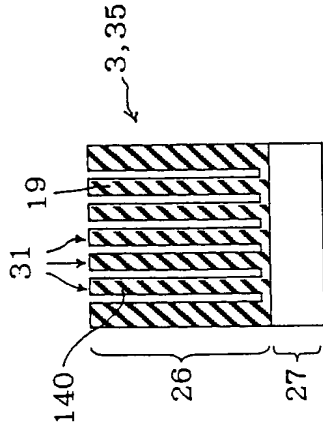


FIG. 4(c)

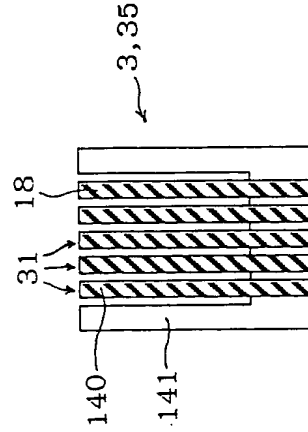


FIG. 4(d)

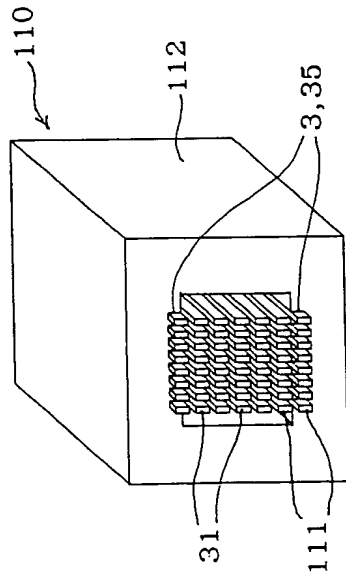


FIG. 4(a)

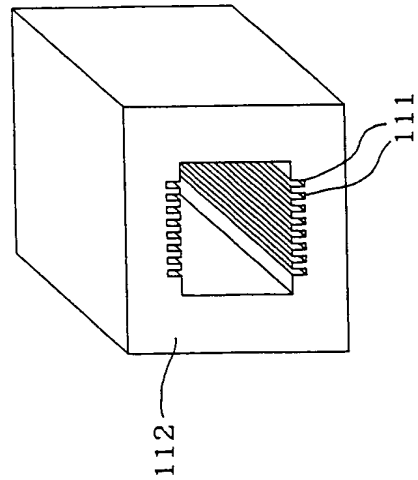
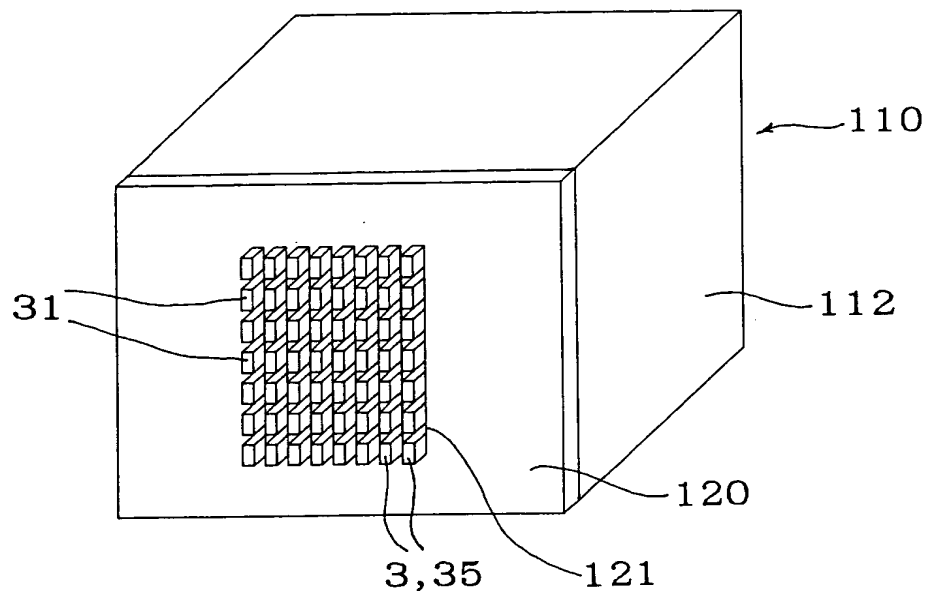


FIG. 4(b)

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FIG. 5



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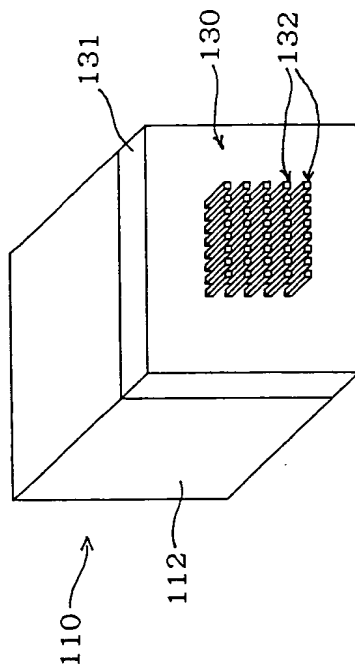


FIG. 6(a)

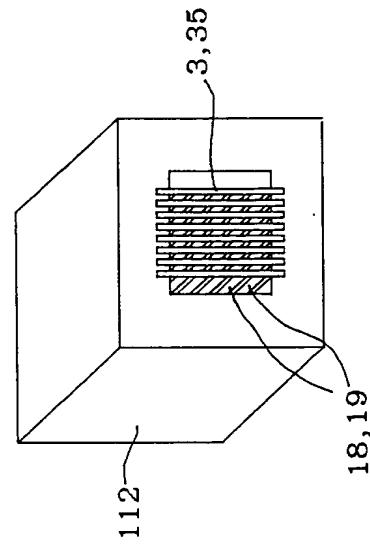


FIG. 6(b)

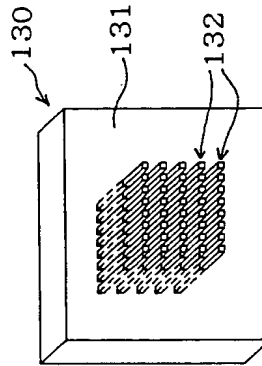
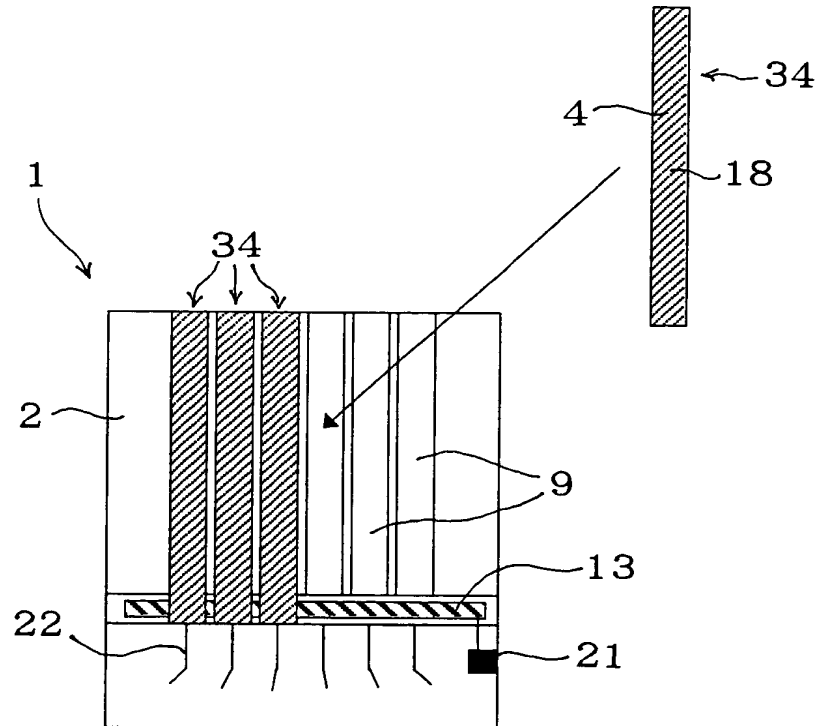


FIG. 6(c)

FIG. 7



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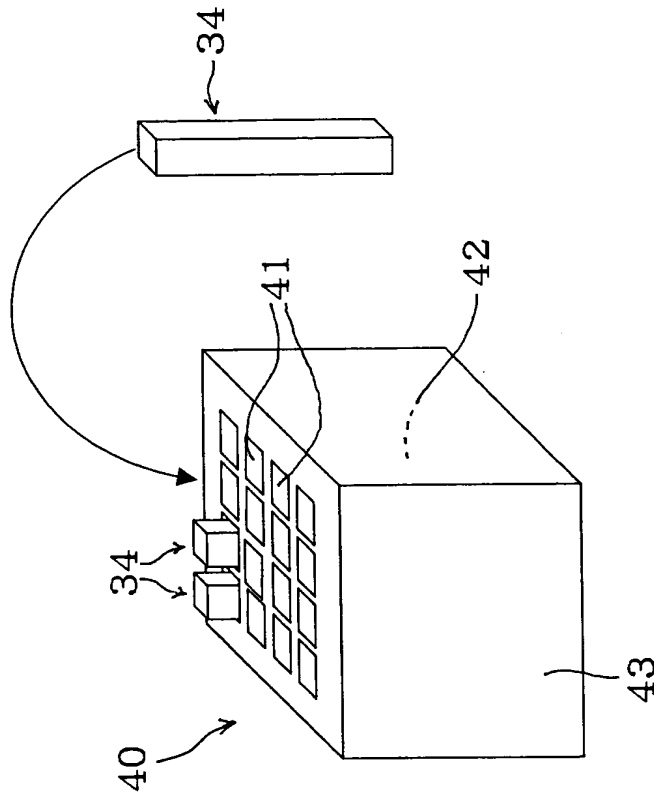


FIG. 8(b)

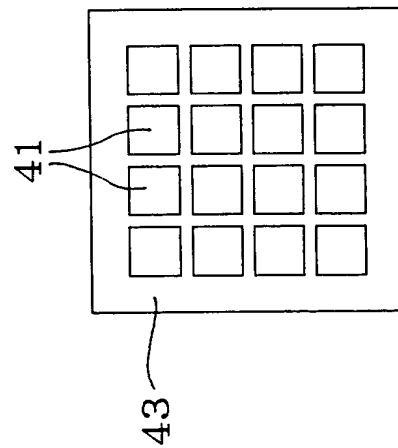


FIG. 8(a)



FIG. 9(a)

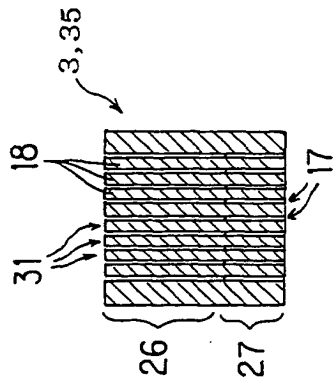


FIG. 9(b)

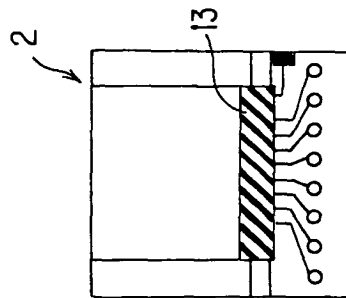


FIG. 9(c)

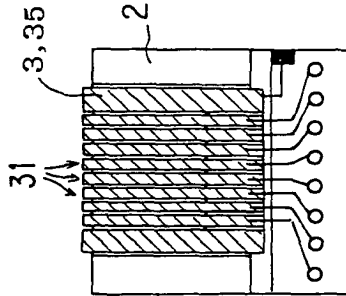


FIG. 9(d)

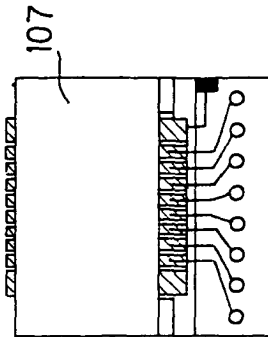
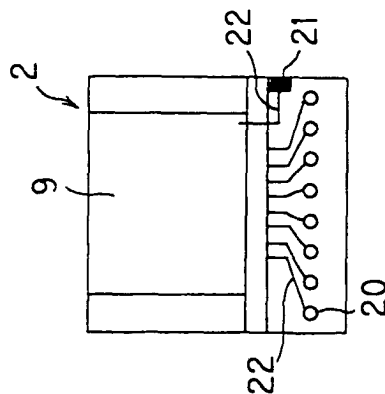


FIG. 9(e)



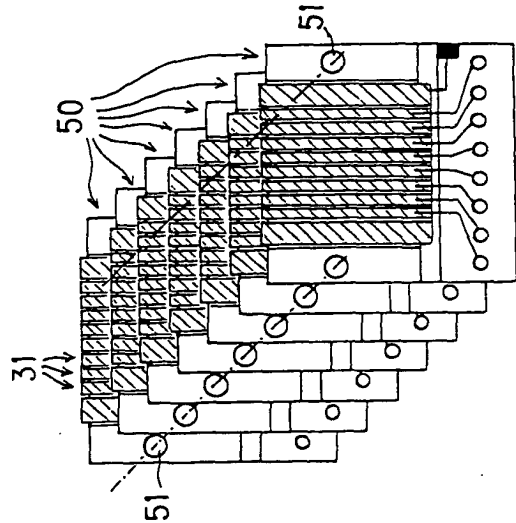


FIG. 10(a)

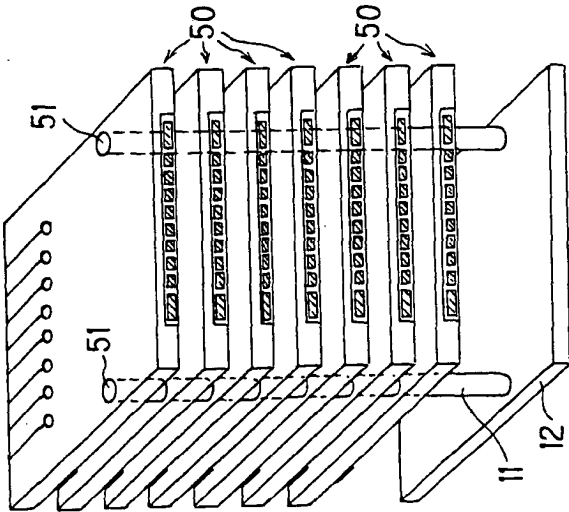


FIG. 10(b)

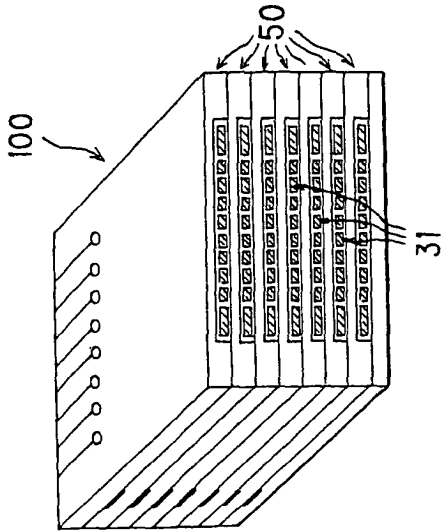
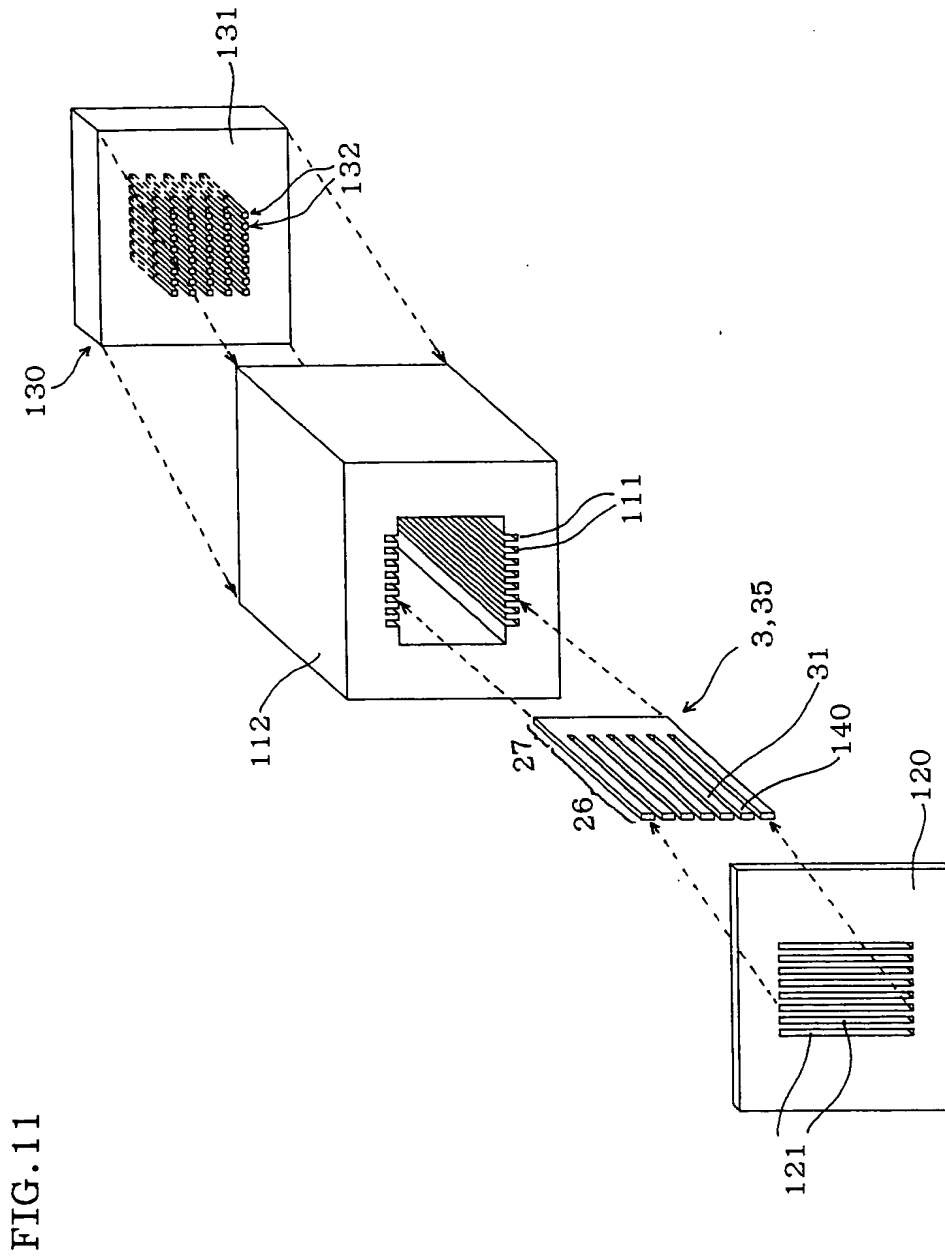


FIG. 10(c)

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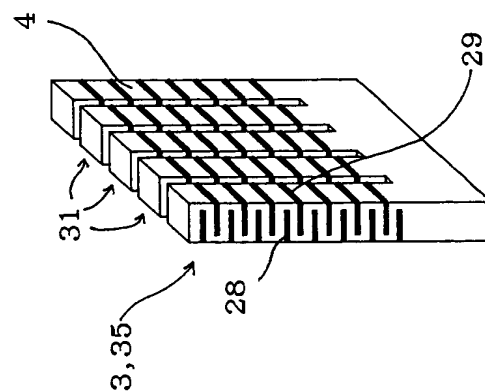


FIG. 12(a)

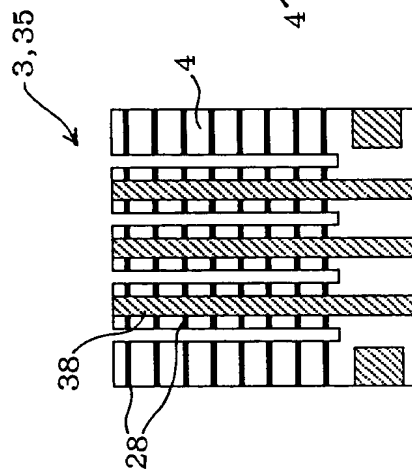


FIG. 12(b)

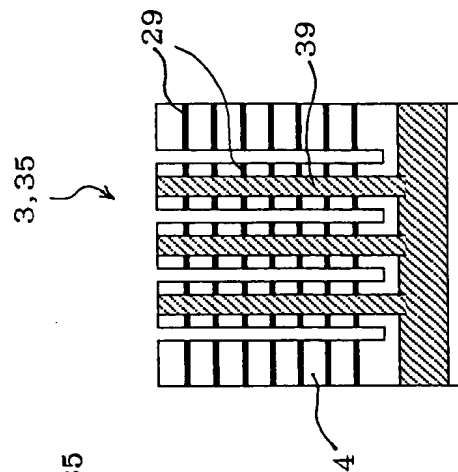


FIG. 12(c)

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FIG. 13(a)

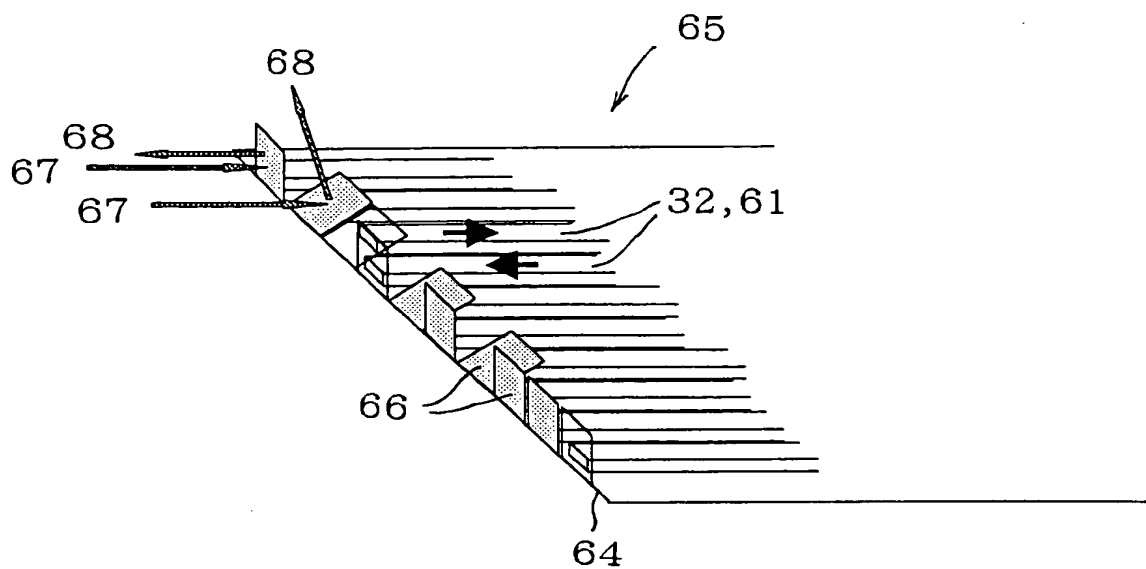
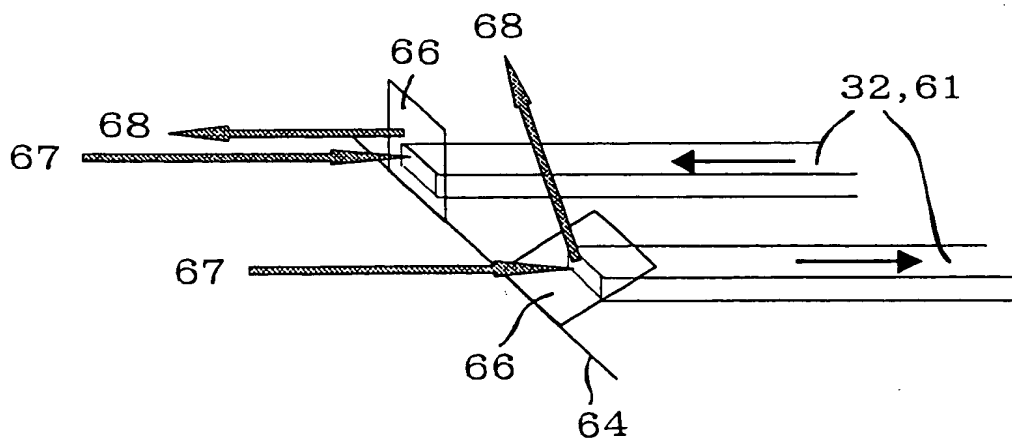
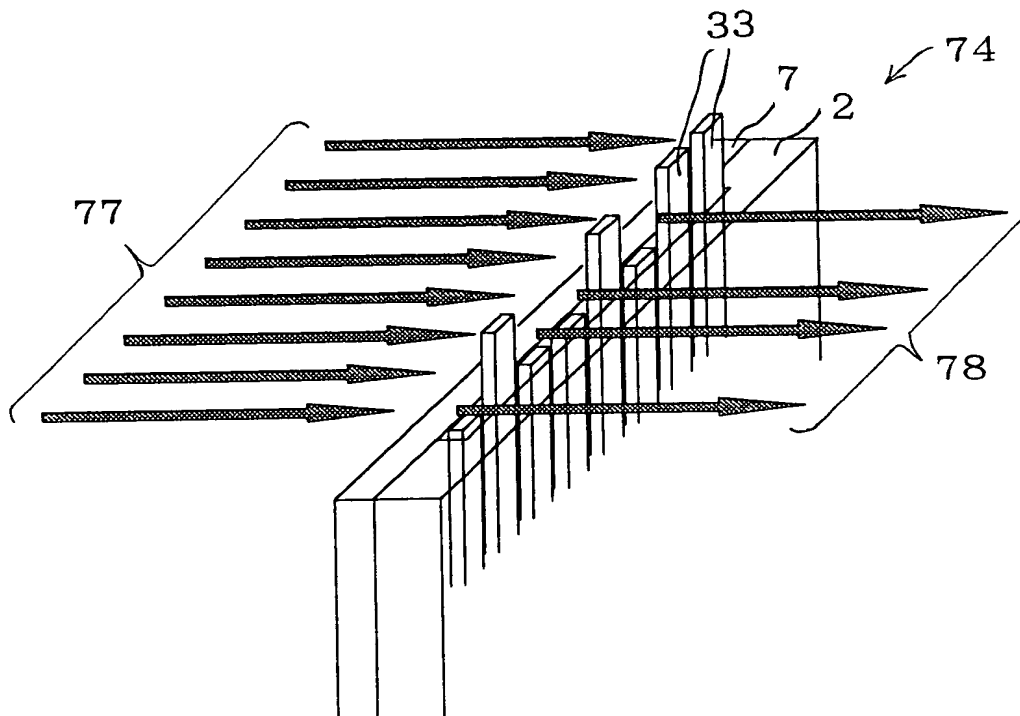


FIG. 13(b)



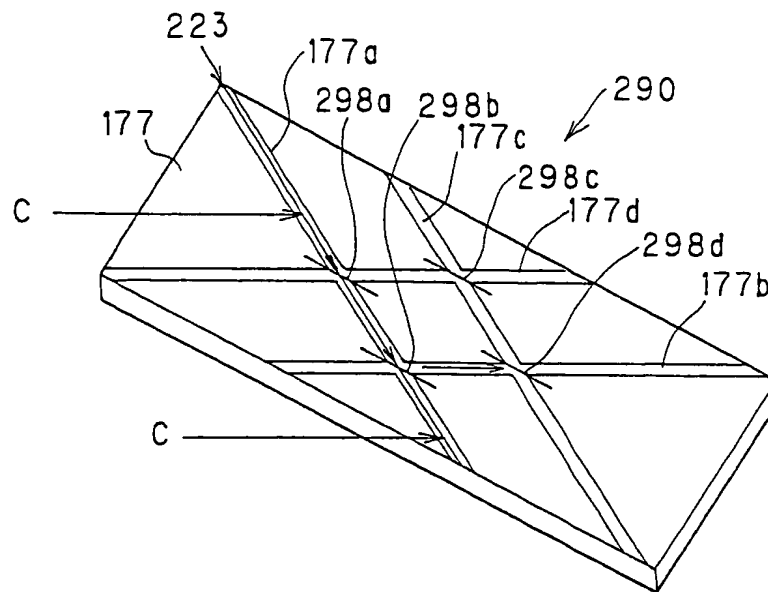
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FIG. 14



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FIG.15



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FIG. 16

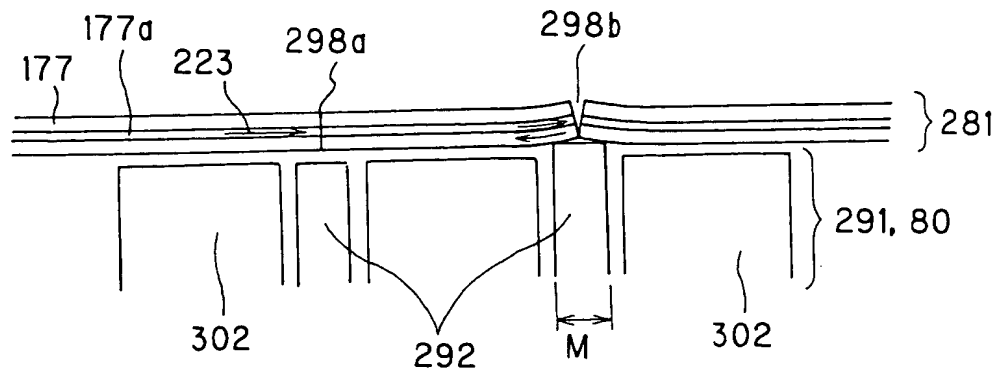
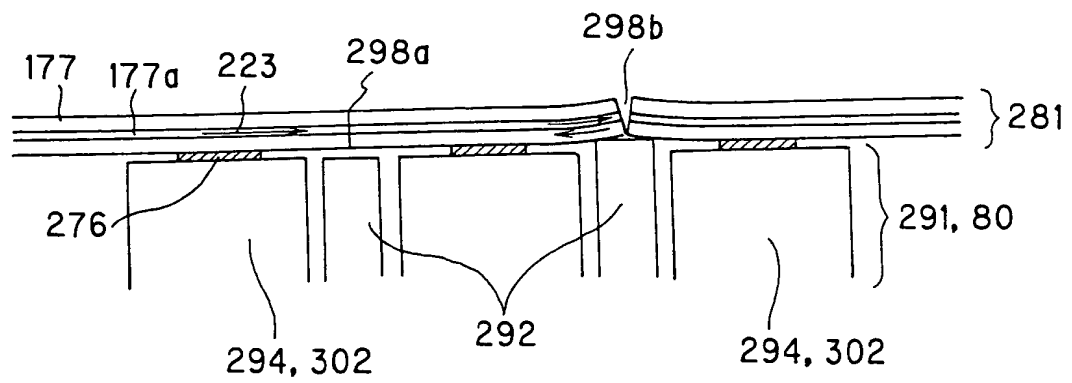


FIG. 17





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FIG. 18

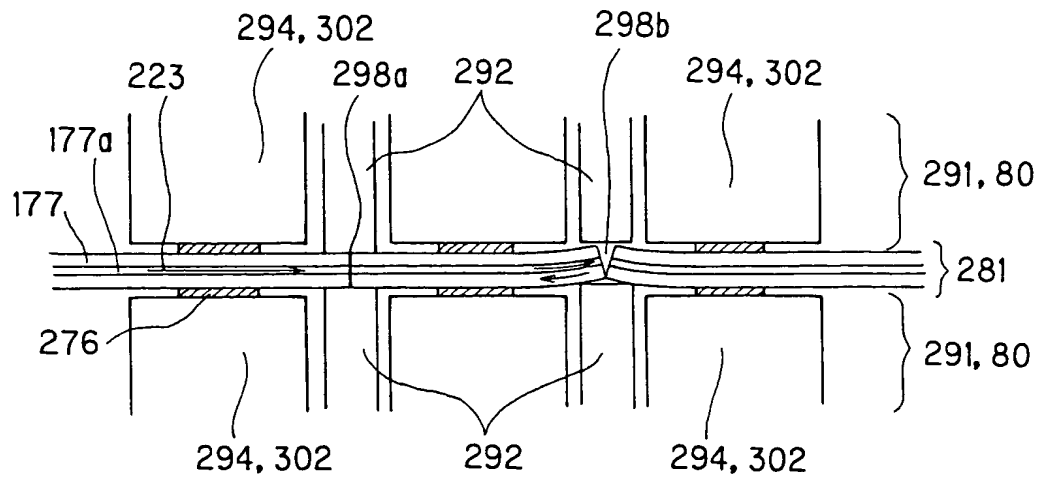


FIG. 19

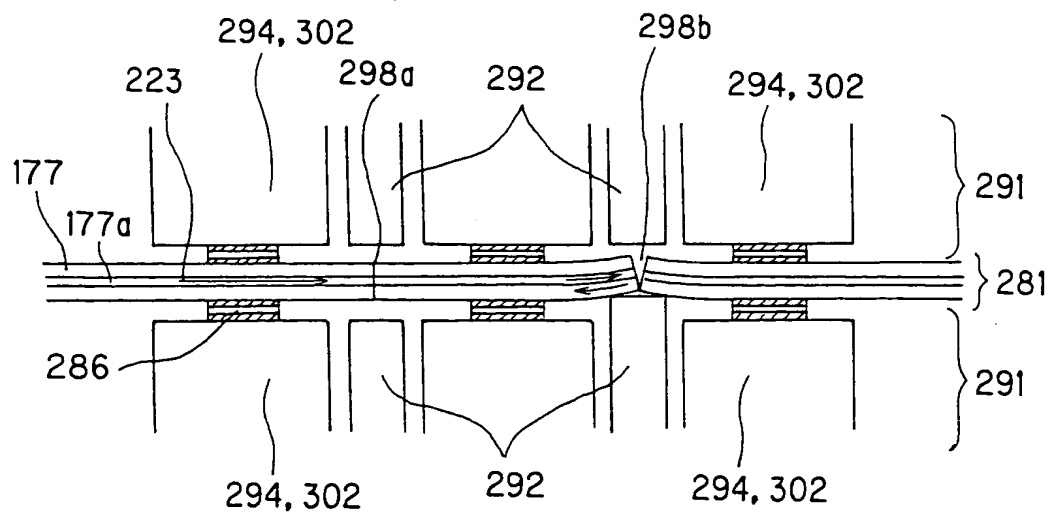


FIG. 20(a)

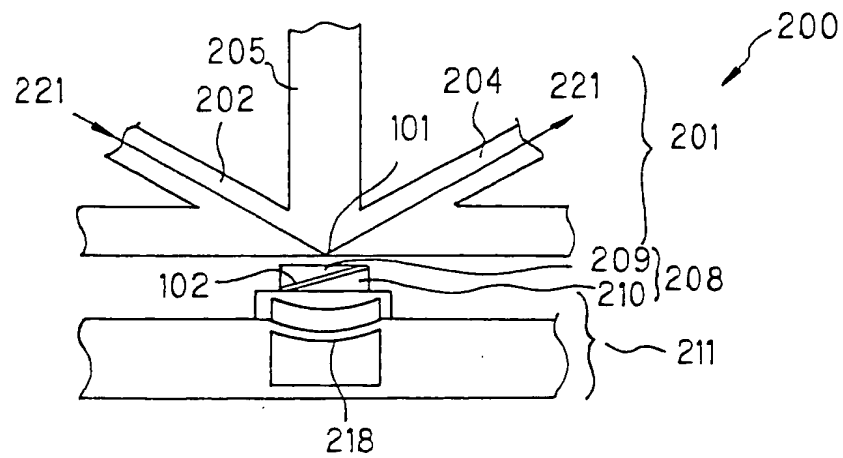


FIG. 20(b)

